



ROBERT STĘPIEŃ
HURTOWNIA CZĘŚCI ELEKTRONICZNYCH
podzespoly-elektroniczne.pl

MJ15004;ON;TO3;tranzystor; PNP;20A;140V;250W;2MHz;RoHS



Dane techniczne:

Nazwa: MJ15004

Typ tranzystora: bipolarny

Kierunek przewodnictwa: PNP

Prąd kolektora: 20A

Napięcie kolektor-emiter: 140V

Moc: 250W

Częstotliwość: 2MHz

Montaż: przewlekany(THT)

Obudowa: TO3

Producent: ON

www.podzespoly-elektroniczne.pl

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An IATF 16949, ISO9001 and ISO 14001 Certified Company



Complimentary Silicon Power Transistors

20 AMPERES, 140 VOLTS, 250 WATTS

NPN MJ15003

PNP MJ15004



TO-3

TO-3

Metal Can Package

RoHS compliant

FEATURES:

1. High DC Current Gain- $h_{FE} = 1000$ (Min) @ $I_C = 25$ Adc
 $h_{FE} = 400$ (Min) @ $I_C = 50$ Adc
2. Curves to 100 A (Pulsed)
3. Diode Protection to Rated I_C
4. Monolithic Construction with Built-In Base-Emitter Shunt Resistor
5. Junction Temperature to $+200^\circ\text{C}$

APPLICATIONS: For use as output devices in complementary general purpose amplifier applications.

ABSOLUTE MAXIMUM RATINGS ($T_a = 25^\circ\text{C}$)

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CEO}	140	Vdc
Collector-Base Voltage	V_{CBO}	140	Vdc
Emitter-Base Voltage	V_{EBO}	5	Vdc
Collector Current — Continuous	I_C	20	Adc
Base Current — Continuous	I_B	5	Adc
Emitter Current — Continuous	I_E	25	Adc
Total Power Dissipation @ $T_C = 25^\circ\text{C}$ Derate above 25°C	P_D	250 1.43	Watts $\text{W}/^\circ\text{C}$
Operating and Storage Junction Temperature Range	T_J, T_{stg}	-65 to $+200$	$^\circ\text{C}$

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	R_{JC}	0.70	$^\circ\text{C}/\text{W}$
Maximum Lead Temperature for Soldering Purposes: 1/16" from Case for ≤ 10 seconds	T_L	265	$^\circ\text{C}$

MJ15003_4

Rev0_06052020EM



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ELECTRICAL CHARACTERISTICS (T_A=25 ° C unless otherwise specified)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector Emitter Sustaining Voltage (1) (I _C = 200 mAdc, I _B = 0)	V _{CEO(sus)}	140	—	Vdc
Collector Cutoff Current (V _{CE} = 140 Vdc, V _{BE(off)} = 1.5 Vdc) (V _{CE} = 140 Vdc, V _{BE(off)} = 1.5 Vdc, T _C = 150°C)	I _{CEX}	— —	100 2	Adc mAdc
Collector Cutoff Current (V _{CE} = 140 Vdc, I _B = 0)	I _{CEO}	—	250	Adc
Emitter Cutoff Current (V _{EB} = 5 Vdc, I _C = 0)	I _{EBO}	—	100	Adc
SECOND BREAKDOWN				
Second Breakdown Collector Current with Base Forward Biased (V _{CE} = 50 Vdc, t = 1 s (non repetitive)) (V _{CE} = 100 Vdc, t = 1 s (non repetitive))	I _{S/b}	5 1	— —	Adc
ON CHARACTERISTICS				
DC Current Gain (I _C = 5 Adc, V _{CE} = 2 Vdc)	h _{FE}	25	150	
Collector Emitter Saturation Voltage (I _C = 5 Adc, I _B = 0.5 Adc)	V _{CE(sat)}	—	1	Vdc
Base Emitter On Voltage (I _C = 5 Adc, V _{CE} = 2 Vdc)	V _{BE(on)}	—	2	Vdc
DYNAMIC CHARACTERISTICS				
Current Gain — Bandwidth Product (I _C = 0.5 Adc, V _{CE} = 10 Vdc, f _{test} = 0.5 MHz)	f _T	2	—	MHz
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f _{test} = 1 MHz)	c _{ob}	—	1000	pF

(1) Pulse Test: Pulse Width = 300 s, Duty Cycle ≤ 2%.



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TYPICAL CHARACTERISTICS CURVES

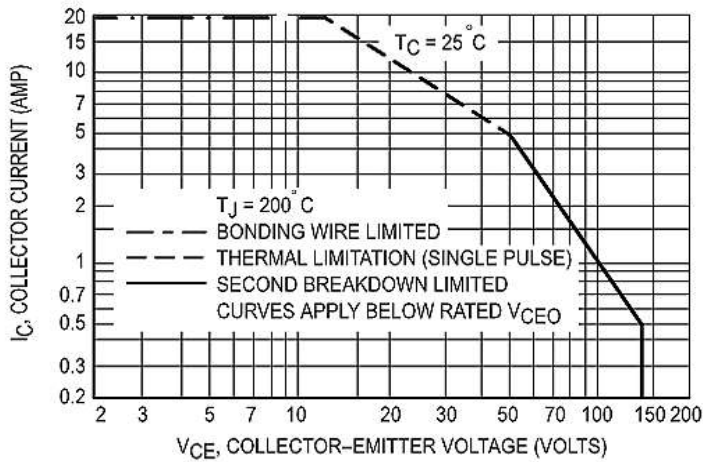


Figure 1. Active-Region Safe Operating Area

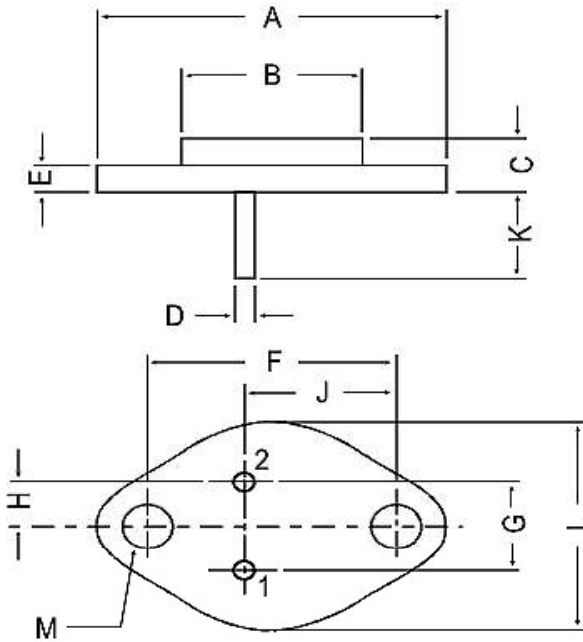


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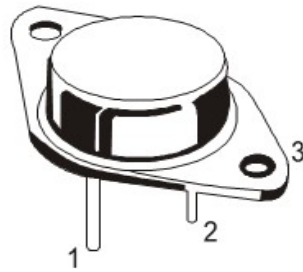


Package Details



All dimensions in mm.

DIM	MIN.	MAX.
A	—	39.37
B	—	22.22
C	6.35	8.50
D	0.96	1.09
E	—	1.77
F	29.90	30.40
G	10.69	11.18
H	5.20	5.72
J	16.64	17.15
K	11.15	12.25
L	—	26.67
M	3.84	4.19



PIN CONFIGURATION

1. BASE
2. EMITTER
3. COLLECTOR

Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-3	100 pcs/pkt	1.3 kg/100 pcs	12.5" x 8" x 1.8"	0.1K	17" x 11.5" x 21"	2K	27.5 kgs

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Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- Temperature 5 °C to 30 °C
- Humidity between 40 to 70 %RH
- Air should be clean.
- Avoid harmful gas or dust.
- Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- Avoid rapid change of temperature.
- Avoid condensation.
- Mechanical stress such as vibration and impact shall be avoided.
- The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down.
They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start.

For this, the following JEDEC table may be referred:

JEDEC MSL Level		
Level	Time	Condition
1	Unlimited	≤30 °C / 85% RH
2	1 Year	≤30 °C / 60% RH
2a	4 Weeks	≤30 °C / 60% RH
3	168 Hours	≤30 °C / 60% RH
4	72 Hours	≤30 °C / 60% RH
5	48 Hours	≤30 °C / 60% RH
5a	24 Hours	≤30 °C / 60% RH
6	Time on Label(TOL)	≤30 °C / 60% RH



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Customer Notes

Component Disposal Instructions

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information.

Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



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