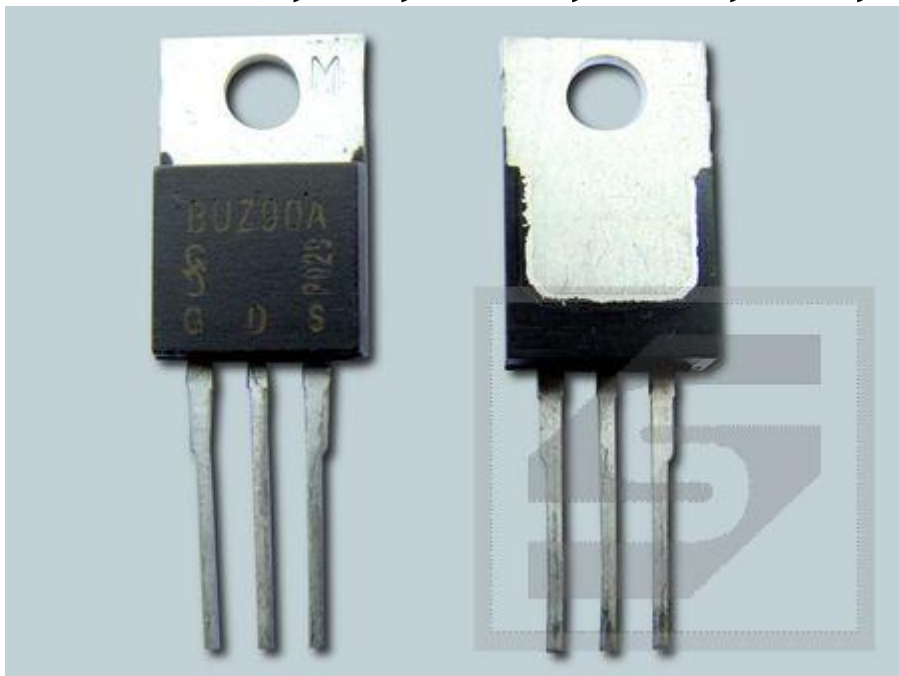




ROBERT STĘPIEŃ
HURTOWNIA CZĘŚCI ELEKTRONICZNYCH
podzespoly-elektroniczne.pl

TR BUZ90A;FSC;TO220;tranzystor; N-MOSFET;4A;600V;75W;Pbf;



Dane techniczne:

Nazwa: BUZ90A

Typ tranzystora: unipolarny

Kierunek przewodnictwa: N-MOSFET

Prąd kolektora: 4A

Napięcie kolektor-emiter: 600V

Moc: 75W

Montaż: przewlekany(THT)

Obudowa: TO220

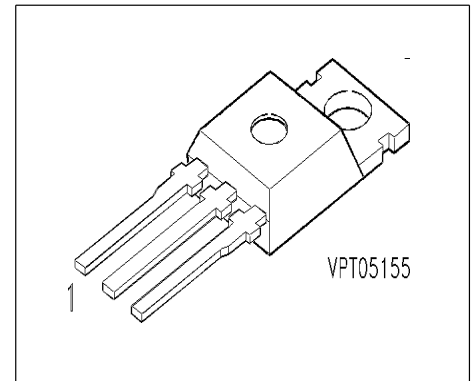
Producent: FSC

www.podzespoly-elektroniczne.pl

Robert Stępień Hurtownia Części Elektronicznych; Adres: ul. Wolumen 2, pawilon 71; 01-912 Warszawa; tel.: 601 296 402 / sklep@podzespoly-elektroniczne.pl

SIPMOS[®] Power Transistor

- N channel
- Enhancement mode
- Avalanche-rated



Pin 1	Pin 2	Pin 3
G	D	S

Type	V _{DS}	I _D	R _{DS(on)}	Package	Ordering Code
BUZ 90 A	600 V	4 A	2 Ω	TO-220 AB	C67078-S1321-A3

Maximum Ratings

Parameter	Symbol	Values	Unit
Continuous drain current T _C = 30 °C	I _D	4	A
Pulsed drain current T _C = 25 °C	I _{Dpuls}	16	
Avalanche current, limited by T _{jmax}	I _{AR}	4.5	
Avalanche energy, periodic limited by T _{jmax}	E _{AR}	8	mJ
Avalanche energy, single pulse I _D = 4.5 A, V _{DD} = 50 V, R _{GS} = 25 Ω L = 29 mH, T _j = 25 °C	E _{AS}	320	
Gate source voltage	V _{GS}	± 20	V
Power dissipation T _C = 25 °C	P _{tot}	75	W
Operating temperature	T _j	-55 ... + 150	°C
Storage temperature	T _{stg}	-55 ... + 150	
Thermal resistance, chip case	R _{thJC}	≤ 1.67	K/W
Thermal resistance, chip to ambient	R _{thJA}	75	
DIN humidity category, DIN 40 040		E	
IEC climatic category, DIN IEC 68-1		55 / 150 / 56	

Electrical Characteristics, at $T_j = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Static Characteristics					
Drain- source breakdown voltage $V_{GS} = 0\text{ V}$, $I_D = 0.25\text{ mA}$, $T_j = 25\text{ }^{\circ}\text{C}$	$V_{(BR)DSS}$	600	-	-	V
Gate threshold voltage $V_{GS}=V_{DS}$, $I_D = 1\text{ mA}$	$V_{GS(th)}$	2.1	3	4	
Zero gate voltage drain current $V_{DS} = 600\text{ V}$, $V_{GS} = 0\text{ V}$, $T_j = 25\text{ }^{\circ}\text{C}$ $V_{DS} = 600\text{ V}$, $V_{GS} = 0\text{ V}$, $T_j = 125\text{ }^{\circ}\text{C}$	I_{DSS}	- -	0.1 10	1 100	μA
Gate-source leakage current $V_{GS} = 20\text{ V}$, $V_{DS} = 0\text{ V}$	I_{GSS}	-	10	100	nA
Drain-Source on-resistance $V_{GS} = 10\text{ V}$, $I_D = 2.8\text{ A}$	$R_{DS(on)}$	-	1.7	2	Ω

Electrical Characteristics, at $T_j = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	

Dynamic Characteristics

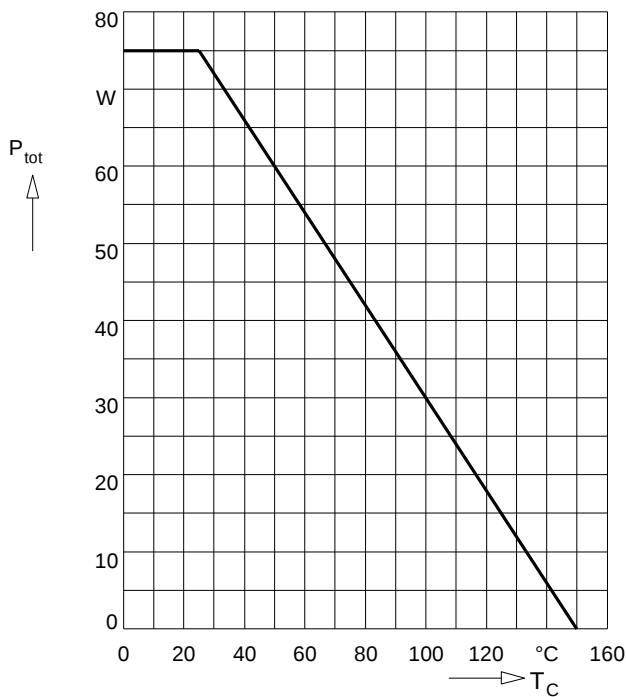
Transconductance $V_{DS} \geq 2 \cdot I_D \cdot R_{DS(on)max}$, $I_D = 2.8 \text{ A}$	g_{fs}	2.5	3.8	-	S
Input capacitance $V_{GS} = 0 \text{ V}$, $V_{DS} = 25 \text{ V}$, $f = 1 \text{ MHz}$	C_{iss}	-	780	1050	pF
Output capacitance $V_{GS} = 0 \text{ V}$, $V_{DS} = 25 \text{ V}$, $f = 1 \text{ MHz}$	C_{oss}	-	110	170	
Reverse transfer capacitance $V_{GS} = 0 \text{ V}$, $V_{DS} = 25 \text{ V}$, $f = 1 \text{ MHz}$	C_{rss}	-	40	70	
Turn-on delay time $V_{DD} = 30 \text{ V}$, $V_{GS} = 10 \text{ V}$, $I_D = 2.6 \text{ A}$ $R_{GS} = 50 \Omega$	$t_{d(on)}$	-	20	30	ns
Rise time $V_{DD} = 30 \text{ V}$, $V_{GS} = 10 \text{ V}$, $I_D = 2.6 \text{ A}$ $R_{GS} = 50 \Omega$	t_r	-	50	75	
Turn-off delay time $V_{DD} = 30 \text{ V}$, $V_{GS} = 10 \text{ V}$, $I_D = 2.6 \text{ A}$ $R_{GS} = 50 \Omega$	$t_{d(off)}$	-	120	150	
Fall time $V_{DD} = 30 \text{ V}$, $V_{GS} = 10 \text{ V}$, $I_D = 2.6 \text{ A}$ $R_{GS} = 50 \Omega$	t_f	-	70	90	

Electrical Characteristics, at $T_j = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Reverse Diode					
Inverse diode continuous forward current $T_C = 25\text{ }^{\circ}\text{C}$	I_S	-	-	4	A
Inverse diode direct current,pulsed $T_C = 25\text{ }^{\circ}\text{C}$	I_{SM}	-	-	16	
Inverse diode forward voltage $V_{GS} = 0\text{ V}$, $I_F = 8\text{ A}$	V_{SD}	-	1.1	1.2	V
Reverse recovery time $V_R = 100\text{ V}$, $I_F=I_S$, $di_F/dt = 100\text{ A}/\mu\text{s}$	t_{rr}	-	350	-	ns
Reverse recovery charge $V_R = 100\text{ V}$, $I_F=I_S$, $di_F/dt = 100\text{ A}/\mu\text{s}$	Q_{rr}	-	3	-	μC

Power dissipation

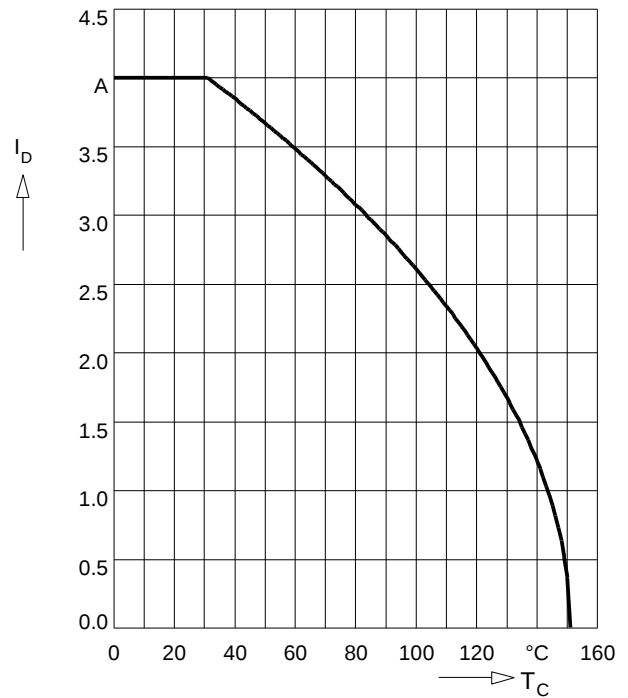
$$P_{\text{tot}} = (T_C)$$



Drain current

$$I_D = (T_C)$$

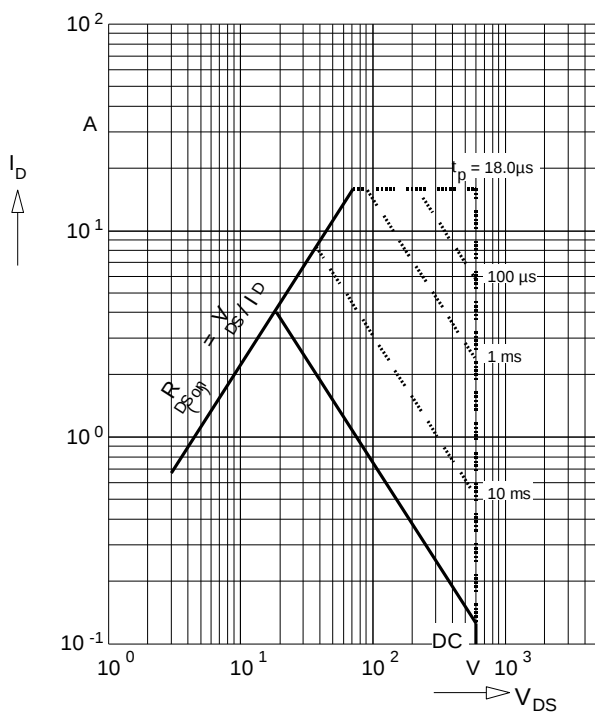
parameter: $V_{GS} \geq 10 \text{ V}$



Safe operating area

$$I_D = (V_{DS})$$

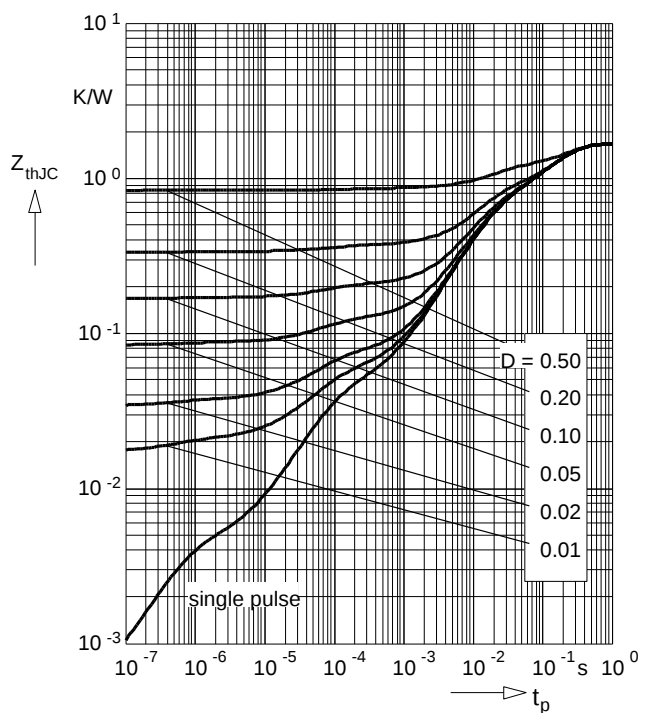
parameter: $D = 0.01$, $T_C = 25^\circ\text{C}$



Transient thermal impedance

$$Z_{thJC} = (t_p)$$

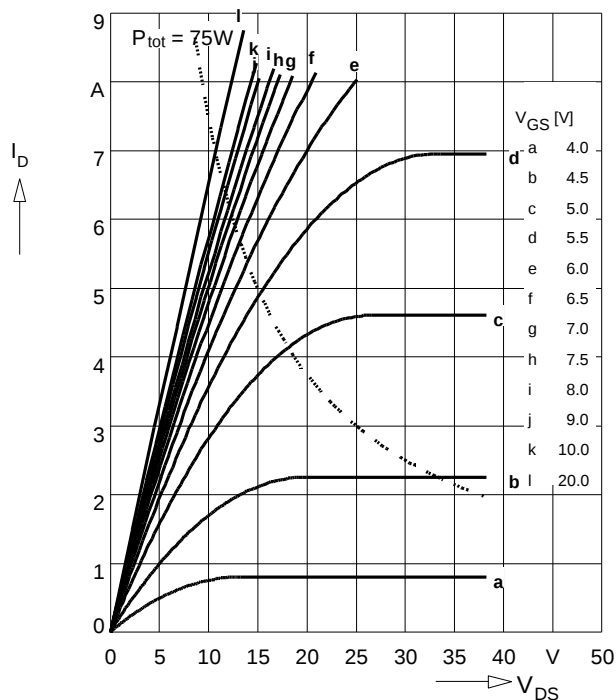
parameter: $D = t_p / T$



Typ. output characteristics

$$I_D = f(V_{DS})$$

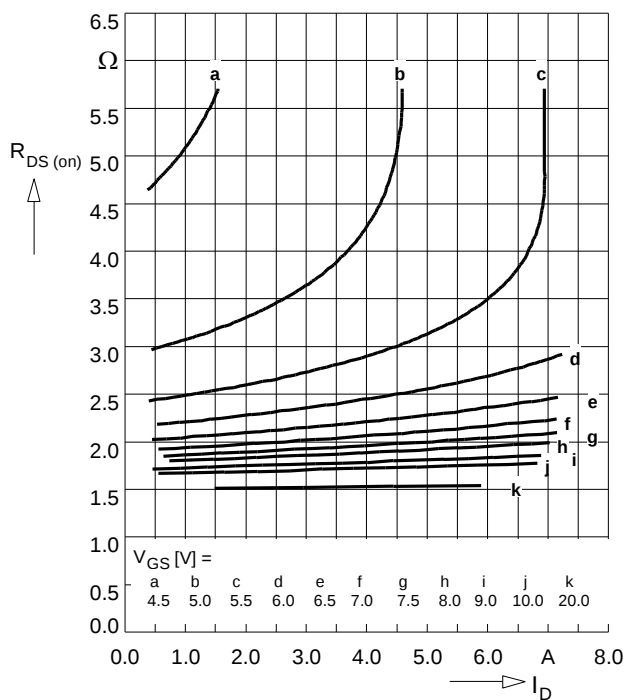
parameter: $t_p = 80 \mu s$



Typ. drain-source on-resistance

$$R_{DS(on)} = f(I_D)$$

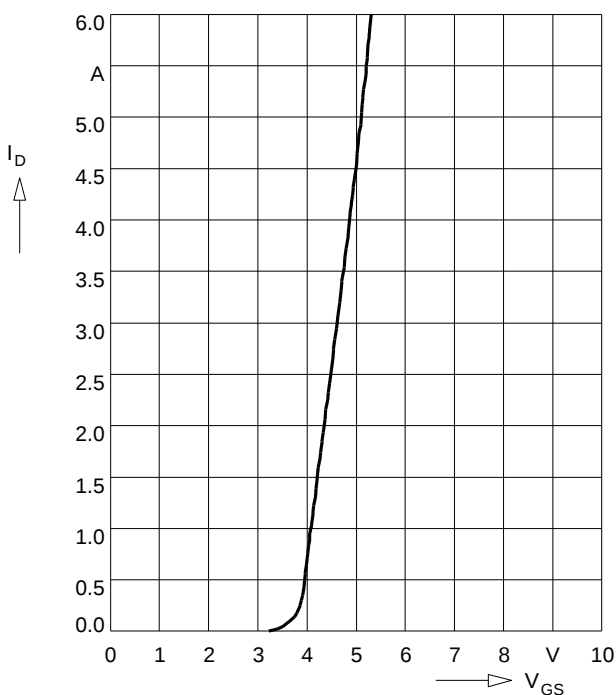
parameter: V_{GS}



Typ. transfer characteristics $I_D = f(V_{GS})$

parameter: $t_p = 80 \mu s$

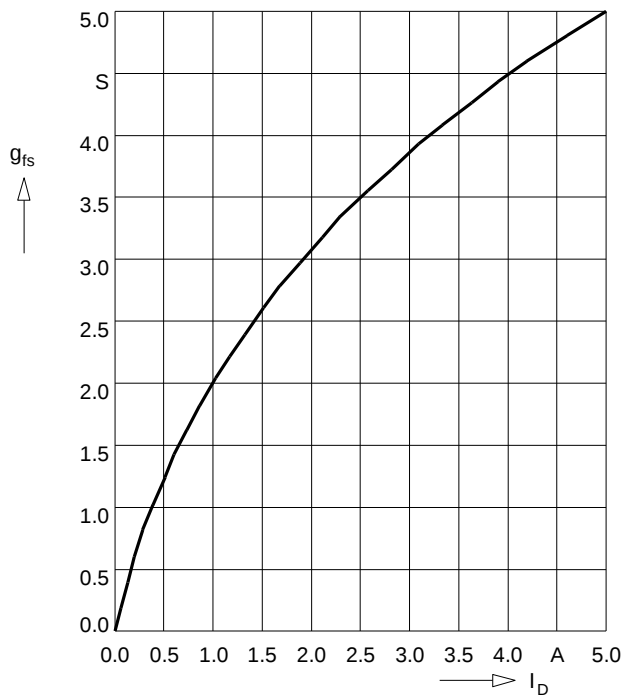
$$V_{DS} \geq 2 \times I_D \times R_{DS(on)max}$$



Typ. forward transconductance $g_{fs} = f(I_D)$

parameter: $t_p = 80 \mu s$,

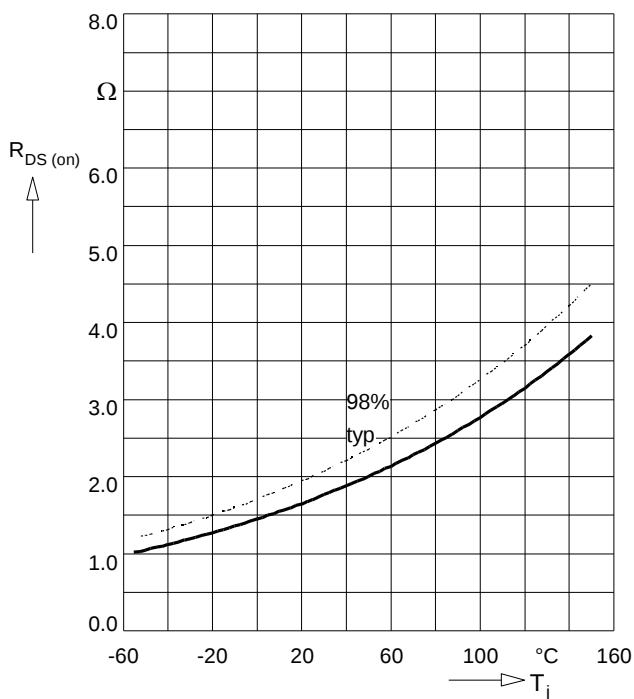
$$V_{DS} \geq 2 \times I_D \times R_{DS(on)max}$$



Drain-source on-resistance

$$R_{DS(on)} = (T_j)$$

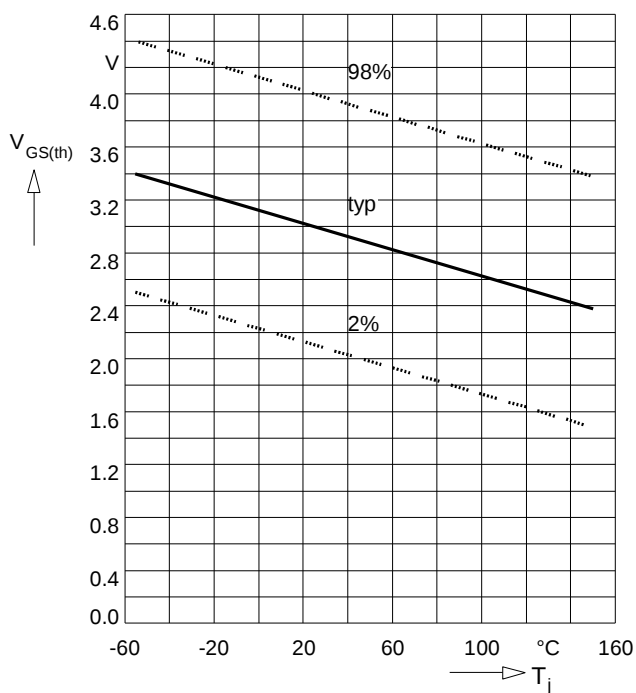
parameter: $I_D = 2.8 \text{ A}$, $V_{GS} = 10 \text{ V}$



Gate threshold voltage

$$V_{GS(th)} = (T_j)$$

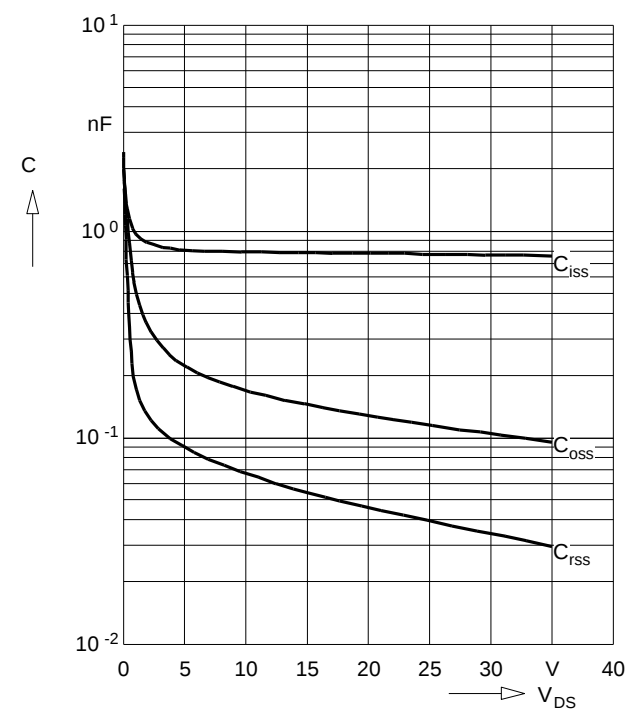
parameter: $V_{GS} = V_{DS}$, $I_D = 1 \text{ mA}$



Typ. capacitances

$$C = f(V_{DS})$$

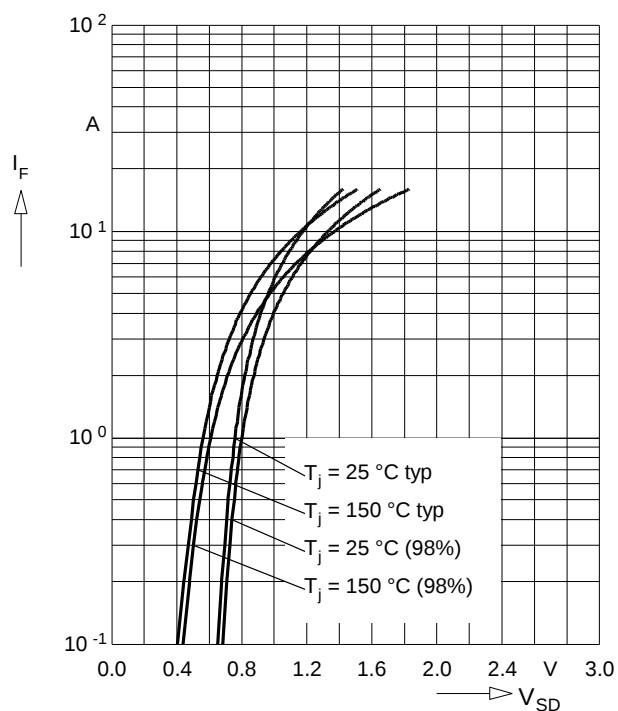
parameter: $V_{GS} = 0 \text{ V}$, $f = 1 \text{ MHz}$



Forward characteristics of reverse diode

$$I_F = (V_{SD})$$

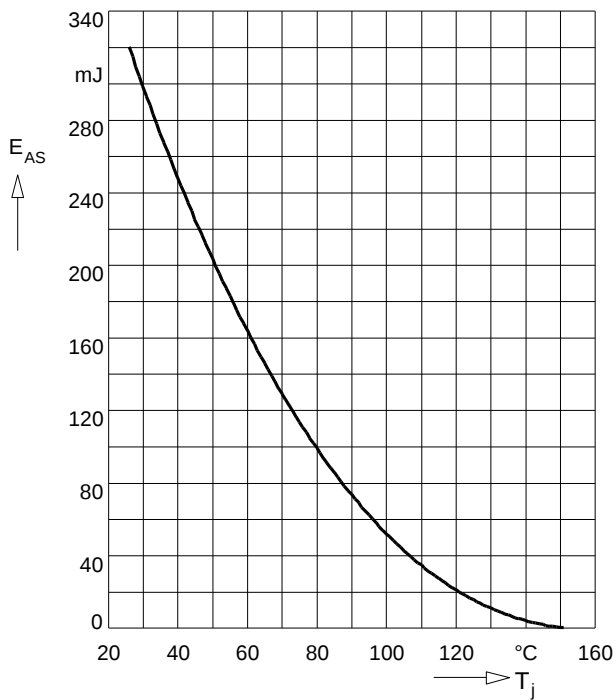
parameter: T_j , $t_p = 80 \mu\text{s}$



Avalanche energy $E_{AS} = (T_j)$

parameter: $I_D = 4.5 \text{ A}$, $V_{DD} = 50 \text{ V}$

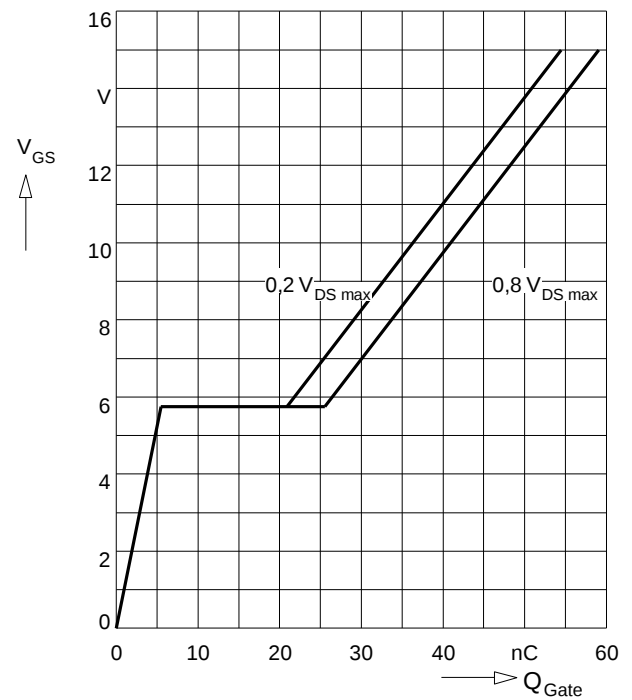
$R_{GS} = 25 \Omega$, $L = 29 \text{ mH}$



Typ. gate charge

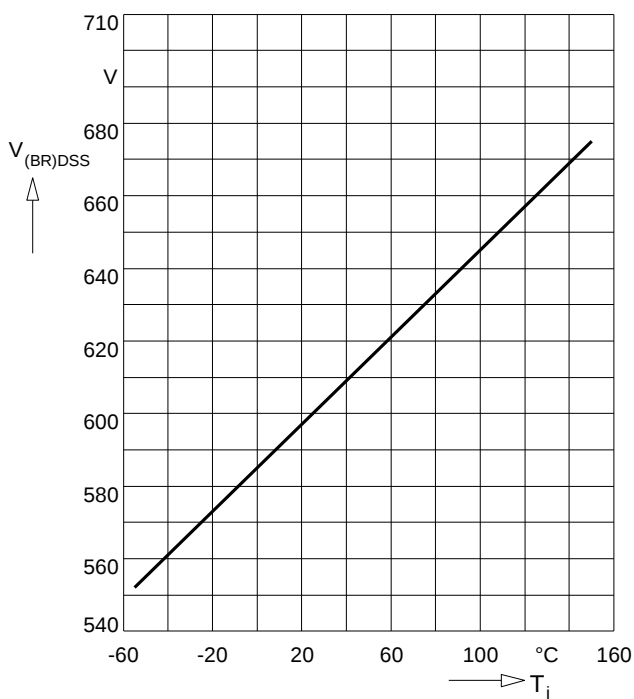
$V_{GS} = (Q_{Gate})$

parameter: $I_{D \text{ puls}} = 7 \text{ A}$



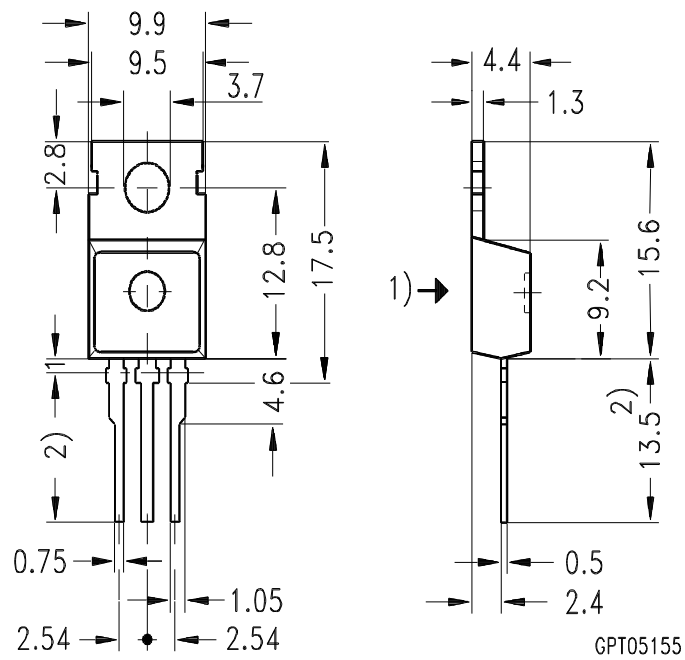
Drain-source breakdown voltage

$V_{(BR)DSS} = (T_j)$



Package Outlines

Dimension in mm



- 1) punch direction, burr max. 0.04
- 2) dip tinning
- 3) max. 14.5 by dip tinning press burr max. 0.05